

IN THE CLAIMS

1. (Original) A method of determining a received signal strength indicator signal from an in-phase signal component and a quadrature signal component of a low intermediate frequency signal that represents a received radio frequency signal, said method comprising:

determining a first absolute value from said in-phase signal component;

determining a second absolute value from said quadrature signal component; and

summing said first and second absolute values.

2. (Original) A method as claimed in Claim 1, further comprising logarithmically processing said in-phase and quadrature signal components before determining said first and second absolute values.

3. (Original) A method as claimed in Claim 2, wherein said logarithmically processing comprises multistage limiting of said in-phase and quadrature signal components, and summing said multistage limited in-phase and quadrature signal components.

4. (Original) A method as claimed in Claim 1, further comprising logarithmically processing said in-phase and quadrature signal components after summing said first and second absolute values.

5. (Original) A method as claimed in Claim 1, wherein said received signal strength indicator signal is further determined by low pass filtering said summed first and second absolute values.

6. (Original) A method as claimed in Claim 1, wherein said low intermediate frequency signal is a zero intermediate frequency signal.

7. (Original) A radio device comprising:

- an antenna for receiving a radio frequency signal;
- a quadrature down converter for producing a low intermediate frequency in-phase signal component and a low intermediate frequency quadrature signal component from said radio frequency signal;

- a received signal strength indicator for producing a received signal strength indicator signal from said low intermediate frequency in-phase and quadrature signal components, said received signal strength indicator comprising a first absolute value former for deriving a first absolute signal from said in-phase signal component, a second absolute value former for deriving a second absolute signal from said quadrature signal component, and an adder for adding said first and second absolute signals.

8. (Original) A radio device as claimed in Claim 7, wherein said received signal strength indicator further comprises a first logarithmic signal former for determining a first logarithmic signal from said in-phase signal component and a second logarithmic signal former for determining a second logarithmic signal from said quadrature signal component, said first absolute signal being said first logarithmic signal and said second absolute signal being said second logarithmic signal.

9. (Original) A radio device as claimed in Claim 8, wherein said first and second logarithmic signal formers comprise respective multistage limiters and respective adders for adding signals produced by said multistage limiters.

10. (Original) A radio device as claimed in Claim 7, wherein said received signal strength indicator further comprises a low pass filter for low pass filtering said added first and second absolute signals.

11. (Original) A radio device as claimed in Claim 7, wherein said low intermediate frequency signal is a zero intermediate frequency.

12. (Original) A radio device as claimed in Claim 7, wherein said received signal strength indicator further comprises a logarithmic signal former for forming a logarithmic signal from said added first and second absolute signals.

13. (Original) A received signal strength indicator for use in radio device with an antenna for receiving a radio frequency signal, a quadrature down converter for producing a low intermediate frequency in-phase signal component and a low intermediate frequency quadrature signal component from said radio frequency signal, and said received signal strength indicator for producing a received signal strength indicator signal from said low intermediate frequency in-phase and quadrature signal components, said received signal strength indicator comprising

 a first absolute value former for deriving a first absolute signal from said in-phase signal component;

 a second absolute value former for deriving a second

absolute signal from said quadrature signal component; and
an adder for adding said first and second absolute
signals.

14. (currently amended) A received signal strength indicator
as claimed in Claim ~~14~~ 13, wherein said low intermediate
frequency signal is a zero intermediate frequency signal.